

[SEMICONDUCTOR STORAGE DEVICE HAVING MULTIPLE INTERRUPT FEATURE FOR CONTINUOUS BURST READ AND WRITE OPERATION]

Abstract

The present invention relates to a semiconductor storage device capable of performing seamless input/output of data when an interrupt is inserted during a burst operation. A plurality of segment arrays activated independently from each other are provided. Each segment array includes a plurality of unit arrays activated independently from each other. Each segment array is provided with prefetch latch circuits for latching burst read data and preload latch circuits for latching burst write data. When an interrupt occurs during a burst operation of a first unit array in a first segment array, a second unit array in a second activated segment array initiates a burst operation.